



## **Corporate Fact Sheet**

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### **The Company**

SiliconBlue® is a pioneer and leader in ultra-low power, single-chip, SRAM-based Field Programmable Gate Arrays (FPGAs). The company's iCE65™ mobileFPGAs™ have set new standards in programmable logic solutions by combining ultra low power, low cost and small form factor into a single-chip solution. SiliconBlue's mobileFPGAs are the premier programmable logic solution for consumer, battery-based mobile applications such as smart phones, eBooks/ePaper, netbooks, digital picture frames, mobile internet devices (MIDs), portable media players, hand-held POS systems, medical instruments and flash camcorders. These applications have an extremely short development and product life cycle therefore, by combining the reprogramming benefit of FPGAs with the low cost, low power and single-chip benefits of ASICs, the iCE65 mobile FPGA offers the best-of-both-worlds solution for the handheld consumer system designer.

### **The Market**

Consumer device convergence has been in full swing since the introduction of the smart phone. This is the most powerful trend-shaping consumer handheld product today with further convergence spreading to new applications such as eBooks, digital picture frames, UMPCs, Netbooks and MIDs. Enabling the drive towards product convergence is the need from system designers to integrate multiple, new functions into consumer handheld devices using chips that must be ultra-low power, very small and low cost. SiliconBlue's iCE65 mobileFPGAs are optimized to meet all three criteria, while also providing designers the added benefits of programmable logic: shorter development time, zero NREs and the ability to make design changes any time in the product life cycle.

According to industry market research firm iSuppli (March '09), shipments of consumer mobile internet devices (MIDs) are projected to grow at a compound annual growth rate of 50.6 percent between 2007 and 2012, reaching 416 million units. SiliconBlue participates in these consumer handheld markets by successfully delivering high value logic solutions. As a result, the company will help drive overall global FPGA TAM by expanding applications in high volume, battery powered markets.

### **The Products**

With introduction of the iCE65 mobileFPGA family, SiliconBlue became the world's first company to deliver an ultra-low power, single-chip SRAM FPGA. The portfolio offers 14 different device options optimized for logic density, power, I/O count and package size. Operating power as low as 8μW make the iCE65 mobileFPGAs ideal for applications where optimizing or extending battery life is key. Logic capacity ranges from ~100K to ~800K FPGA system gates, I/O counts from 128 to 384 and package sizes as small as 3mm by 4mm provide the best solution for each logic design need. SiliconBlue provides



easy to use, power-optimized EDA software tools (VHDL/Verilog) that increase productivity, shorten design cycles and achieve design performance and power requirements quickly and efficiently. In addition, a broad portfolio of >50 silicon-proven IP elements (IP blocks and turn-key designs) and complete development kits enable designers to accelerate time-to-market and reduce design integration risk.

Also available in die form, the ultra low power “ASIC-like” mobileFPGAs provide system designers and ASSP vendors with a new, high density “3-D stacking” option (SiPs). Using industry standard techniques, designers can now stack their current application processor chips with the iCE65 mobileFPGA chips and deliver new products, with new features, quickly and easily without waiting 12-18 months for their next generation ASSP. This innovative, best-of-both-breeds stacking solution also significantly reduces design costs, risk and improves time-to-market.

### **The Technology**

Not only was SiliconBlue the first high-tech company to produce ultra low power, single-chip SRAM-based FPGAs, they also were the first to combine low power, Non-Volatile Configuration Memory (NVCM) with standard SRAM technologies at the 65nm LP process node. As one of four different configuration options, the unique, on-chip NVCM provides users with the lowest cost, single chip production solution. This single chip solution (identical to ASICs/ASSPs) eliminates the need for external flash PROMs, saves part count, board space, and cost while providing effective design security. Fabricated at the industry’s largest pure-play foundry, TSMC, the LP process can be directly scaled to 40nm and 28nm geometries. SiliconBlue also carries a valuable portfolio of more than 40 patents.

### **The History**

Headquartered in Santa Clara, California, the privately held company was funded in 2006 and employs more than 70 people worldwide. SiliconBlue has IC / software / IP design & development locations in the Santa Clara (California), Chengdu (China), Manipal and Bangalore (India), and sales offices maintained in China, Korea, Japan, Taiwan, Europe and North America. The company’s capital-efficient business model includes a partner ecosystem which leverages expertise from industry leaders such as TSMC (foundry services), ASE (test/assembly) and Magma (EDA software).

### **Contact Information**

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